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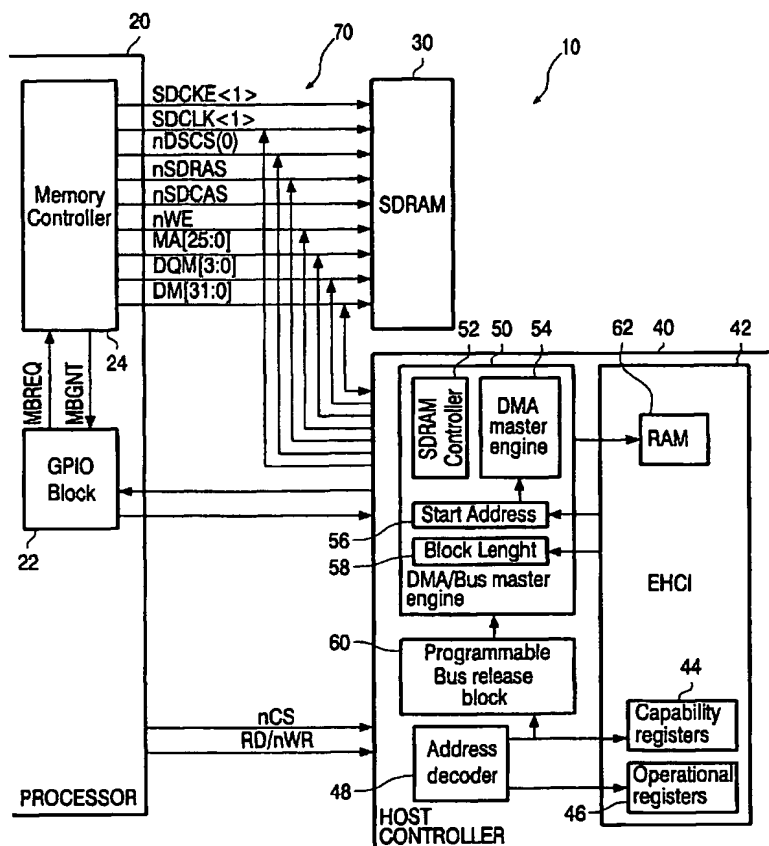
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(54) Title: **UBS HOST CONTROLLER WITH DMA CAPABILITY**



(57) Abstract: An embedded host controller, for use in a USB system comprising a processor and an associated system memory, comprises a DMA controller, and the host controller is adapted such that, in order to retrieve data from the associated system memory, a starting address and block length are sent to the DMA controller, and the DMA controller is adapted such that, on receipt of a starting address and block length sent from the host controller, it retrieves the indicated data from the associated system memory. This has the advantage that the embedded host controller can be used with different host microprocessors, without assuming that PCI functionality is available.



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BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG)

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B. FIELDS SEARCHED

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2002/178310 A1 (NOZAKI AKIHIRO) 28 November 2002 (2002-11-28) paragraph '0007!	1,2,6,7, 11,12
X	US 6 266 715 B1 (LOYER BRUCE A ET AL) 24 July 2001 (2001-07-24) column 6, line 49 - column 7, line 12; claim 13 column 5, line 19 - line 26	1,2,5
A	US 4 987 529 A (CRAFT THOMAS W ET AL) 22 January 1991 (1991-01-22) figure 4 column 4, line 62 - line 66 column 5, line 31 - line 36	3,4,8-10



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Patent family members are listed in annex.

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INTERNATIONAL SEARCH REPORT

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